LAN8710 Customer Evaluation Board

+3.3V I/O VDDIO Operation

MII Communication

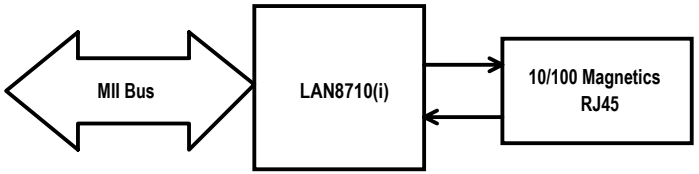
Schematic Revision B03

Design Details

Board:
Chip: LAN8710(i)
Board Form Factor: MII
Assembly: 7169AZ

Circuit Diagrams utilizing SMSC Products Are Included As A Means Of Illustrating Typical Semiconductor Applications: Consequently Complete Information Sufficient For Construction Purposes Is Not Necessarily Given. The Information Has Been Carefully Checked And Is Believed To Be Entirely Reliable. However, No Responsibility Is Assumed For Inaccuracies. Furthermore, Such Information Does Not Convey To The Purchaser Of The Semiconductor Devices Described Any License Under The Patent Rights Of SMSC Or Others. SMSC Reserves The Right To Make Changes At Any Time In Order To Improve Design And Supply The Best Product Possible.

BLOCK DIAGRAM

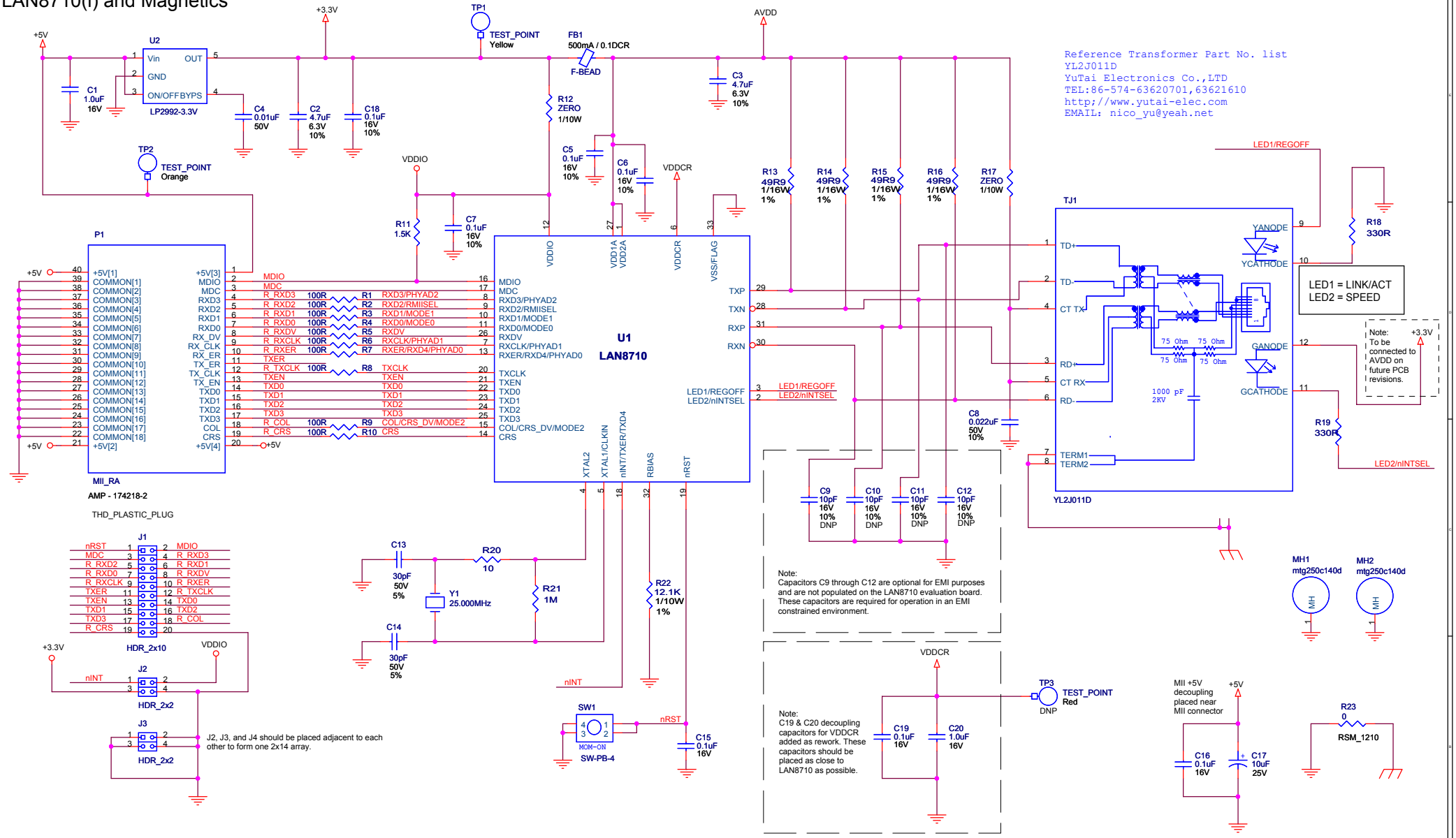


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LAN8710(i) & Magnetics	2

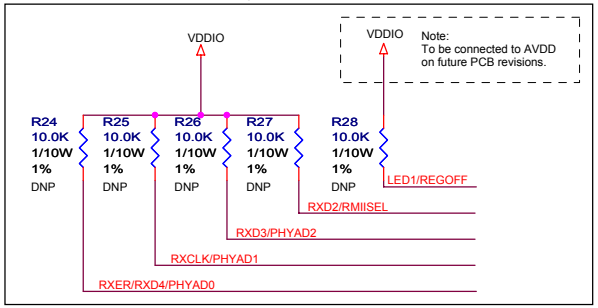
SMSC SUCCESS BY DESIGN

Title				
LAN8710 MII Evaluation Board				
Size	Engineer	Assembly No.	PCB Rev	Schematic Rev
	J.M.	7169AZ	B00	03
Date:	Wednesday, January 06, 2010		Sheet	1 of 2

LAN8710(i) and Magnetics



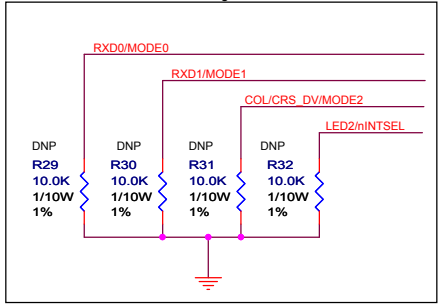
Pull-up Configuration Jumpers



Configuration Resistor Settings

Resistor	POPULATE	EMPTY
R24	PHYAD[0] = 1	PHYAD[0] = 0
R25	PHYAD[1] = 1	PHYAD[1] = 0
R26	PHYAD[2] = 1	PHYAD[2] = 0
R27	RMII mode selected	MII mode selected
R28	Internal 1.2V reg. disabled	Internal 1.2V reg enabled
R29	MODE[0] = 0	MODE[0] = 1
R30	MODE[1] = 0	MODE[1] = 1
R31	MODE[2] = 0	MODE[2] = 1
R32	INTERRUPT FUNCTION DISABLED ON nINT/TXER/TXD4 SIGNAL	INTERRUPT FUNCTION ENABLED ON nINT/TXER/TXD4 SIGNAL

Pull-down Configuration Resistors



SMSC SUCCESS BY DESIGN

Title				
LAN8710 / Magnetics / MII Connector				
Size	Engineer	Assembly No.	PCB Rev	Schematic Rev
	J.M.	7169AZ	B00	03
Date:	Wednesday, January 06, 2010		Sheet 2 of 2	